

Lambda-based Design Rules

the design rules and layout methodology based on the concept of λ provides a process and feature size-independent way of setting out mask wires (nmos & pmos)

fig: Design rules for

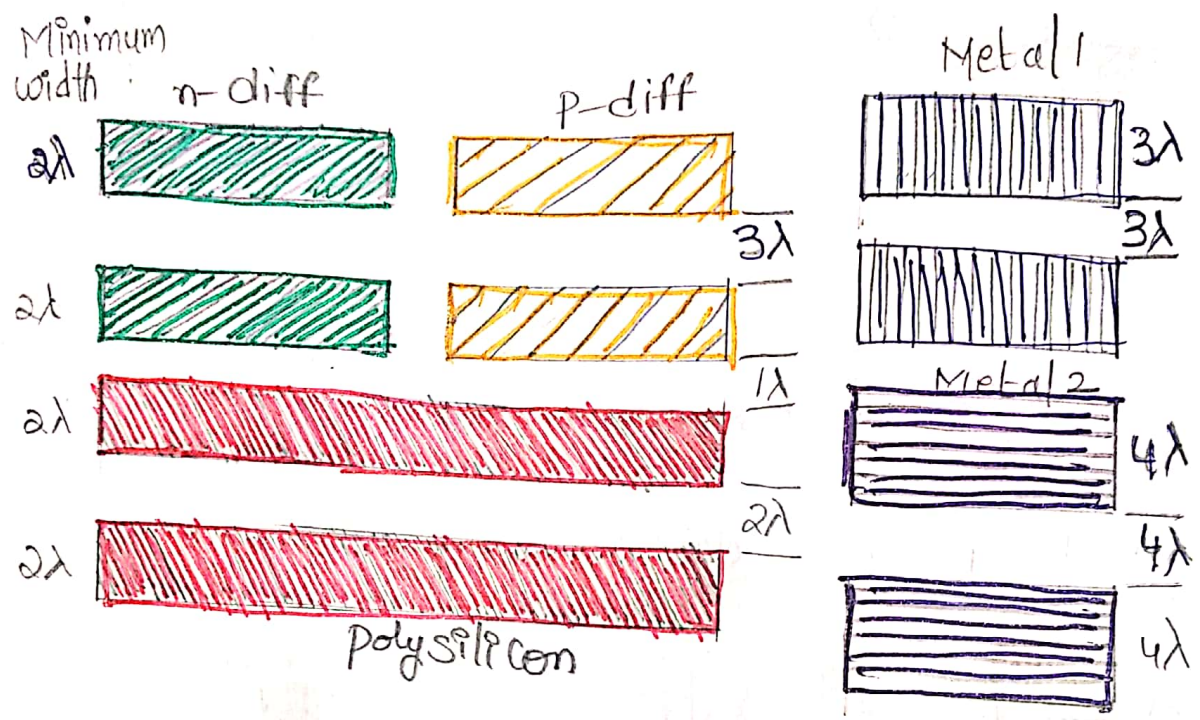
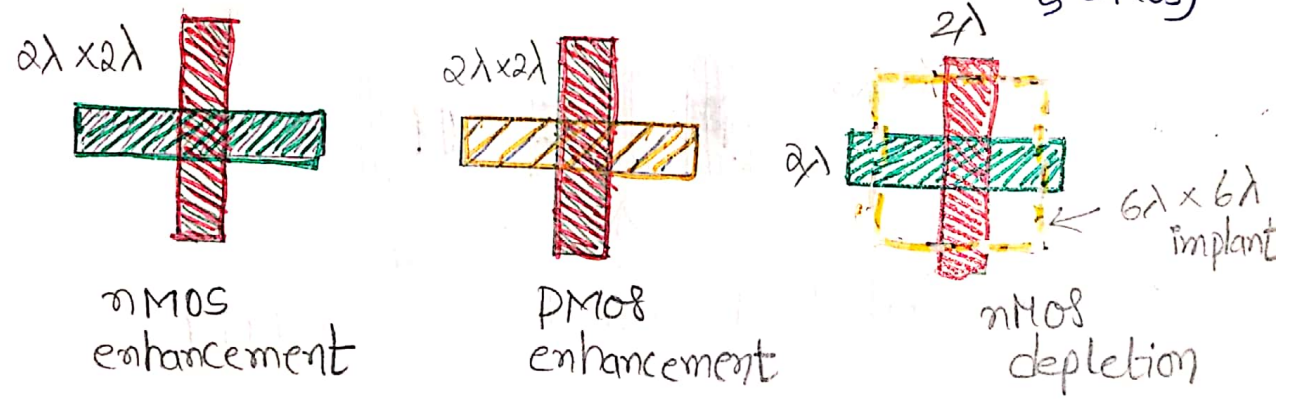


fig: Transistor Design rules (nmos, pmos, & cmos)



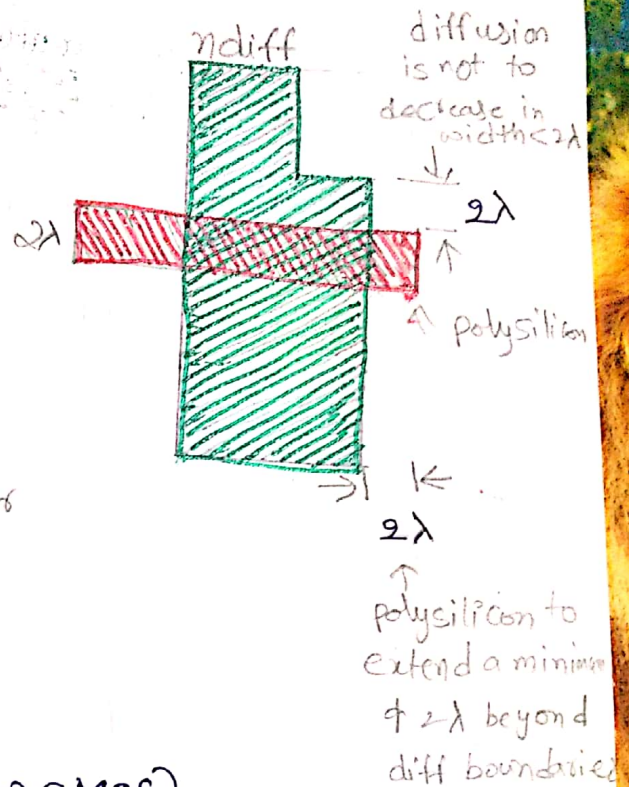
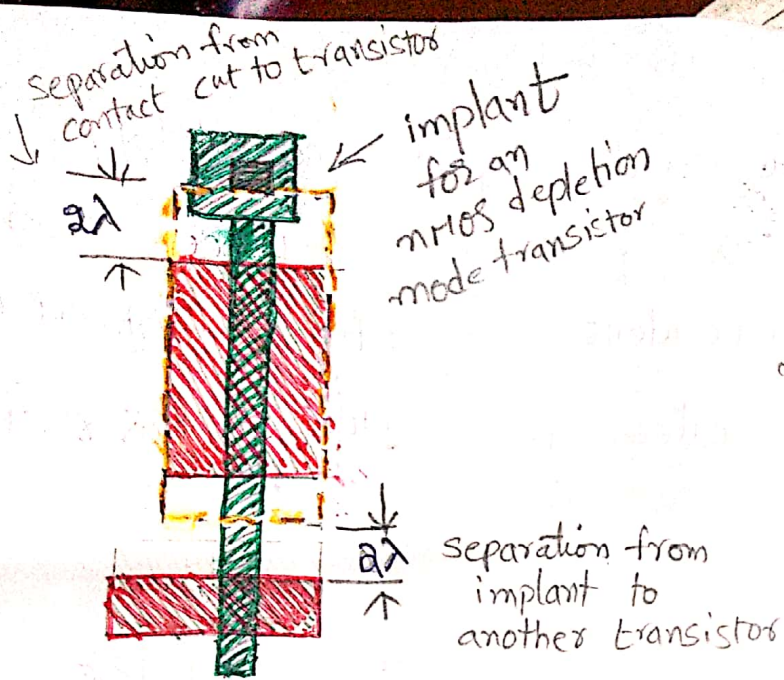
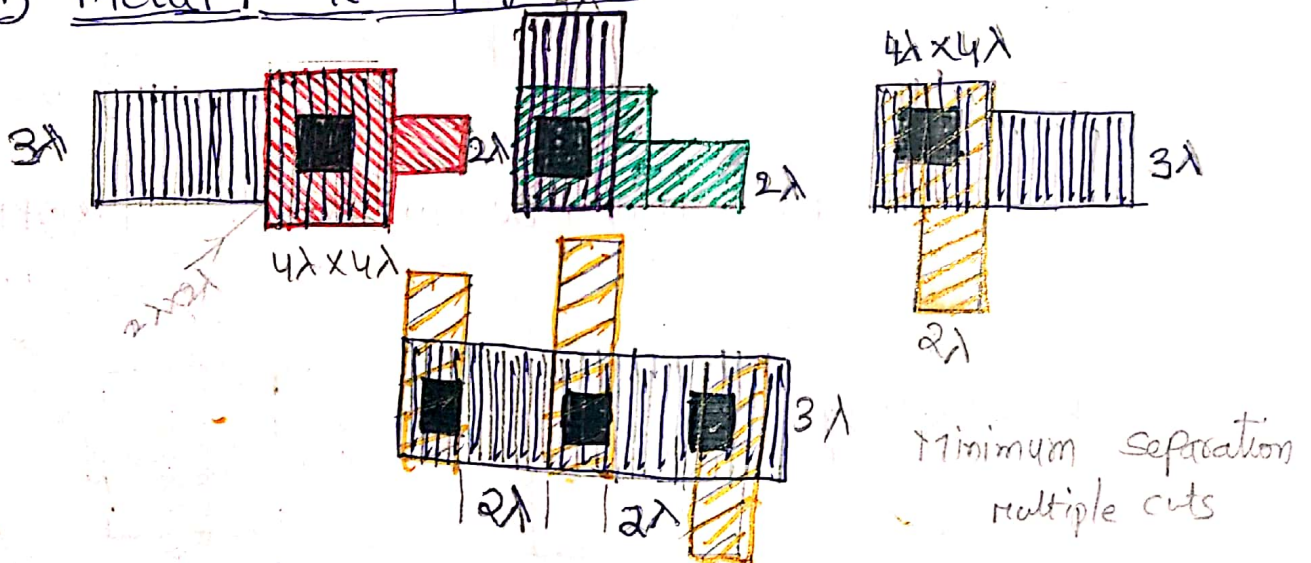


fig: contacts (nMOS & CMOS)

① Metal 1 to polysilicon or to diffusion



② Via Contact from metal 2 to metal 1

